

Photon Emission by Silicon-Based Memristors

Till Zellweger^(1,3,t,*), Bojun Cheng^(1,t), Konstantin Malchow^(2,t), Aymeric Leray⁽²⁾, Jan Aeschlimann⁽³⁾,
Mathieu Luisier⁽³⁾, Alexandros Emboras⁽³⁾, Alexandre Bouhelier⁽²⁾ and Juerg Leuthold^(1,t)

⁽¹⁾ ETH Zurich, Institute of Electromagnetic Fields, Zurich 8092, Switzerland

⁽²⁾ Laboratoire Interdisciplinaire Carnot de Bourgogne, UMR 6303 CNRS, Université de Bourgogne Franche-Comté, Dijon 21078, France

⁽³⁾ ETH Zurich, Integrated Systems Laboratory, Zurich 8092, Switzerland

^(t) These authors contributed equally: Till Zellweger, Bojun Cheng, Konstantin Malchow

*Corresponding authors: till.zellweger@iis.ee.ethz.ch, leuthold@ethz.ch

Abstract *We introduce a new category of nanoscale photon sources based on memristors with silicon-based switching matrices. These novel photon sources exhibit light emission during the switching of their resistive state. The photon emission is attributed to the creation and excitation of silicon nanoclusters. ©2022 The Author(s)*

Introduction

The introduction of a compact CMOS compatible on-chip photon source is a major challenge to this very day. Despite steadily increasing research interest in this field [1], the significant size mismatch between on-chip photon sources and the nanoscale electronic components still greatly hinders their large-scale co-integration [2–4]. This co-integration would be highly desired as on-chip photonics, with its centerpiece the photon source, offers a largely superior bandwidth and lower latency at a lower power dissipation compared to the electronic counterparts [3–5]. As such, incorporating photonics into the CMOS technology would resolve many challenges on the current path of digital electronics towards atomic scale feature sizes and would herald in a next generation of computing [3,5–7].

The currently most promising on-chip photon sources for the near future include lasers based on III-V materials, Germanium, and its alloys. In spite of their performance, these solutions suffer from inherently large feature sizes in the order of micrometers [2,8,9]. At smaller physical dimensions, new photon sources have emerged such as quantum dot based light sources [10–12] and inelastic tunnelling emitters [13–15]. However, whereas electrical injection of carriers in quantum dots is still fundamentally difficult [9], tunnelling emitters require an extremely thin tunnelling barrier that requires advanced nanofabrication [15] or stochastic arrangement [13] limiting their yield and their applicability as on-chip photon sources.

Recently we introduced a novel atomic scale photon source based on a memristor with an oxygen-rich SiO_x ($x < 2$) switching matrix offering both the required nanoscale dimensions as well as a straightforward, scalable and

CMOS compatible fabrication process [16]. It exhibits electroluminescence during the switching of its resistive state.

In this work we demonstrate that this memristive photon emission effect is not limited to oxygen-rich SiO_x but also occurs in Si. We propose that this functionality can be exploited in many Si-based switching matrices by introducing a photon emission mechanism for such Si-based memristive photon sources. As Si-based materials are amongst the most commonly used switching matrices for this memristor technology [17,18], photonic functionalities could be employed within existing memristor applications which would extend their already imposing capabilities in neuromorphic computing and memory technologies [19,20]. Furthermore, this novel category of nanoscale photon sources - even though showing weak emission in these first experiments - has a great potential for large-scale co-integration with digital electronics. This would allow to make use of the advantages of the CMOS technology, photonics and the memristor technology on a single chip.

Device Structure and Operating Principle

The device platform of the memristive photon source is depicted in Fig. 1(a). It consists of a silver and a platinum electrode with amorphous Si (a-Si) or amorphous, oxygen-rich SiO_x ($x > 2$) as switching matrix, fabricated on a glass coverslip. The electrode tips are shaped to form an optical antenna enabling enhanced radiation efficiencies at the emission wavelength. Applying a positive voltage between the silver and platinum electrode causes these devices to switch from a high to a low resistance state by forming a metallic filament as depicted in Fig. 1(b). Such cells are known as electrochemical metallization (ECM)-type memristors [17,18].

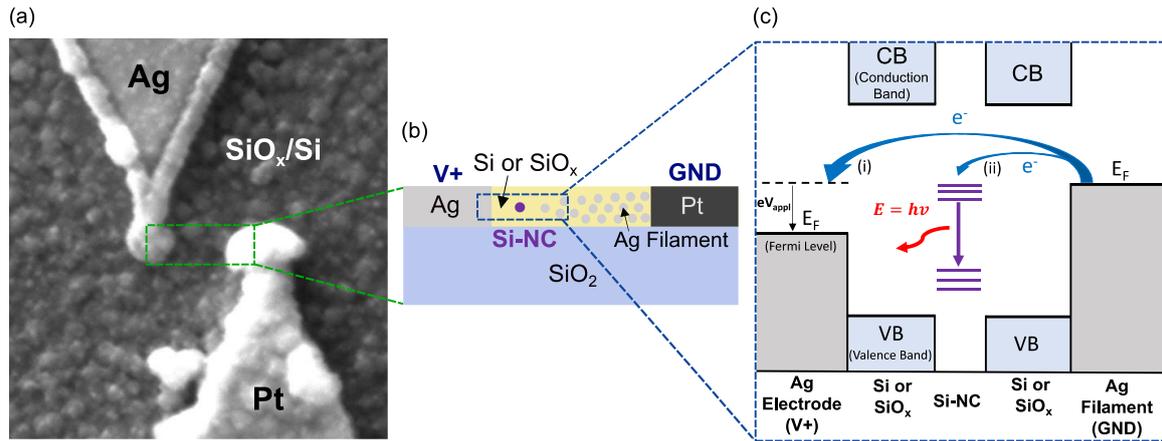


Fig. 1: Device structure and operating principle. (a) SEM picture of the device structure. The photon source consists of structured Ag and Pt electrodes within either an amorphous Si (a-Si) or oxygen-rich, amorphous SiO_x (a-SiO_x) switching matrix. (b) Switching mechanism and creation of defects. Upon applying a voltage to the device, a metallic filament consisting of Ag atoms grows from the Pt towards the Ag electrode. During this process, silicon nanocluster-type defects (Si-NC) are introduced in the switching matrix. (c) Photon emission mechanism. Due to the growing filament, electron tunneling increases drastically. These electrons can either tunnel to the Ag electrode (i) or excite Si-NC defects (ii). In the latter case, radiative recombination occurs, and photons are emitted.

The operating principle of photon emission is based on the creation and excitation of luminescent sites within the switching matrix. More precisely, in a process concurrent with the switching to a low resistance, the applied voltage and the filament formation cause local alterations of the switching matrix between the electrodes (details see *Analysis of Si-NC related photon emission*). This creates luminescent silicon nanocluster-type defects (Si-NC), shown in Fig. 1(b). As illustrated in Fig. 1(c), these created Si-NC can be excited by individual tunnelling electrons (i) that are part of the total tunnelling current (ii). This causes light emission by radiative recombination.

Measurements

In Fig. 2(a) and (b), time-resolved electro-optical measurements of devices with an a-Si and a-SiO_x switching matrix are shown, respectively. The measurements were performed by applying a voltage pulse (black solid line) while concurrently capturing the emitted photons with an avalanche photodiode (APD), see red line. The emission of photons for both switching matrices is clearly a transient phenomenon occurring at the instance when the devices switch from their high to their low resistance state (blue solid line) or in other words at the onset of a forming metallic filament. The number of detected photons in the case of a-Si is lower. This is likely explained by the fact that the optical environment during the photon emission process is drastically different for a-Si compared

to a-SiO_x. Particularly, silicon introduces absorption at the wavelength of emission and causes a reflection due to the refractive index mismatch with the glass substrate, both of which are causing a lower collection efficiency.

In Fig. 2(c), the normalized spectra of two devices with either an a-Si or a-SiO_x switching matrix are shown. The spectra were measured by applying a series of voltage pulses to the device, thereby triggering many switching and thus photon emission events. The emission spectra of the two device types are broadband and largely overlapping at smaller wavelengths with a peak around 740 nm observed in both cases. The spectrum of the a-Si additionally exhibits a main peak at 840 nm. These characteristics of the two spectra are attributed to the peculiarities of silicon nanocluster-related photon emission. Not only is the local optical environment of each cluster slightly different (inhomogeneous broadening) but also the size and especially the interface of the nanoclusters are known to greatly influence the emission wavelength (homogeneous broadening) [21,22]. As a result, we expect many sub-variants of silicon nanoclusters with varying emission wavelengths that are created and excited during the numerous switching events, causing broadband emission. As the conditions of Si-NC formation in the two switching matrices are distinct, a different relative abundance of sub-variants is expected which would explain the shift of the main peak in the two spectra.

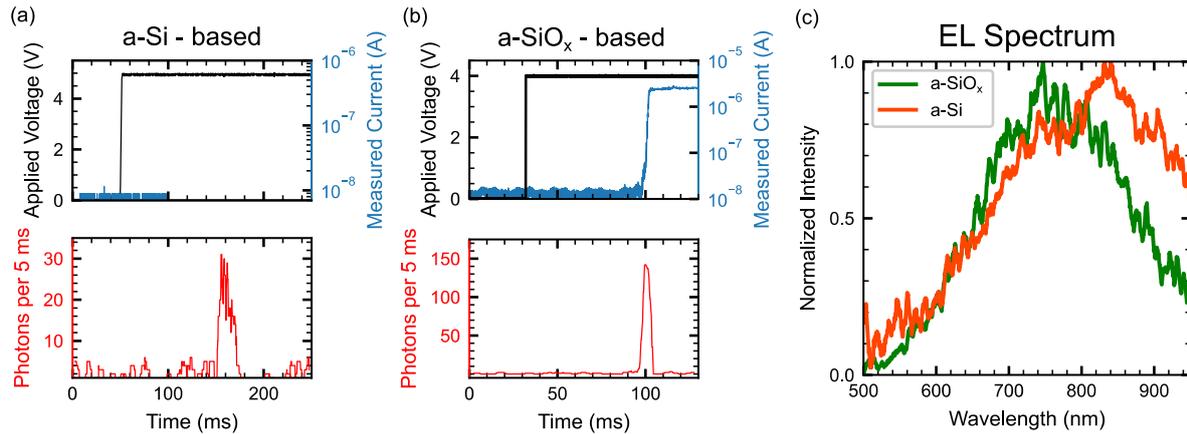


Fig. 2: Electro-optical measurements of the memristive photon sources with a-Si and a-SiO_x switching matrices. (a) and (b) Time-resolved measurements with an a-Si (a) and an a-SiO_x matrix (b). The measurements clearly show a transient photon emission effect that occurs during the switching to a lower resistance state. In both measurements, photons are also apparent before and after the switching. These detection events were attributed to the dark counts of the avalanche photodiode (APD). (c) Electroluminescence spectrum measurements for both switching matrices. The spectra are broadband and largely overlap, indicating the same origin of photon emission. The differences between the two spectra are attributed to different relative contributions of Si-NC variants occurring in the respective switching matrices.

Analysis of Si-NC related photon emission

The formation of the luminescent silicon nanoclusters slightly differs for the two switching matrices. As we previously reported [16], in oxygen-rich SiO_x, oxygen vacancy creation and aggregation occurs in a similar way as in oxide-based RAM (OxRAM) memristors [23]. The aggregation of these oxygen vacancies forms locally Si-rich regions with silicon nanoclusters (Si-NC), which are known to produce electroluminescence (EL) and photoluminescence (PL) at the wavelengths matching the light emission reported in Fig. 2(c).

In the case of pure silicon, the occurrence of silicon nanoclusters is also well established. In fact, the first room temperature PL from silicon nanoclusters was observed in porous Si. It was attributed to the quantum confinement caused by the etching of the silicon to produce the porous structure [22]. As for a-Si there is no quantum confinement as there is no separation of such nanoclusters from the bulk. However, there are numerous reports of resistive switching in a-Si which was attributed to local alterations of the silicon that form a filament of higher conductivity upon application of a bias [24–26]. We propose that this effect of bias-induced alteration of the a-Si leads to the segregation of luminescent Si-NCs. More precisely, Si-defects such as Si-dangling bonds are known to form the (preliminary) interface of silicon nanoclus-

ters, thus separating them from the bulk [21]. These dangling bonds are the dominant intrinsic defects in a-Si and due to their low formation energy they can be created by a low electrical bias [27–29].

In conclusion, in a process concurrent with the switching of the memristive photon source, both oxygen-rich a-SiO_x and a-Si matrices see the formation of Si-NC structures that allow electrical excitation and photon emission.

This creation of localized Si-NC is not restricted to memristors with the switching matrices presented here. The occurrence as well as the creation of these clusters have been reported in silicon-rich SiO_x ($x < 2$) [30,31] and in SiN_x [32,33]. This suggests that the presented photon emission mechanism might be occurring in many Si-based switching matrices.

Conclusion

We demonstrate a new CMOS-compatible device platform for next-generation nanoscale photon sources based on the memristor technology. Light emission is observed in both a-Si and a-SiO_x with very similar emission characteristics. The photon emission was attributed to the creation and excitation of silicon nanoclusters. This effect is expected to occur in many more Si-based switching matrices which would allow to make use of the photon emission capability in many existing memristor technologies.

References

1. A. Karabchevsky, A. Katiyi, A. S. Ang, and A. Hazan, "On-chip nanophotonics and future challenges," *Nanophotonics* **9**, 3733–3753 (2020) DOI: [10.1515/nanoph-2020-0204](https://doi.org/10.1515/nanoph-2020-0204)
2. Z. Zhou, B. Yin, and J. Michel, "On-chip light sources for silicon photonics," *Light: Science & Applications* **4**, e358–e358 (2015) DOI: [10.1038/lsa.2015.131](https://doi.org/10.1038/lsa.2015.131)
3. J. A. Dionne, L. A. Sweatlock, M. T. Sheldon, A. P. Alivisatos, and H. A. Atwater, "Silicon-Based Plasmonics for On-Chip Photonics," *IEEE J. Sel. Top. Quantum Electron.* **16**, 295–306 (2010) DOI: [10.1109/JSTQE.2009.2034983](https://doi.org/10.1109/JSTQE.2009.2034983)
4. D. A. B. Miller, "Rationale and challenges for optical interconnects to electronic chips," *Proc. IEEE* **88**, 728–749 (2000) DOI: [10.1109/5.867687](https://doi.org/10.1109/5.867687)
5. J. Shalf, "The future of computing beyond Moore's Law," *Philosophical Transactions of the Royal Society A: Mathematical, Physical and Engineering Sciences* **378**, 20190061 (2020) DOI: [10.1098/rsta.2019.0061](https://doi.org/10.1098/rsta.2019.0061)
6. C. Prasad, "A Review of Self-Heating Effects in Advanced CMOS Technologies," *IEEE Trans. Electron Devices* **66**, 4546–4555 (2019) DOI: [10.1109/TED.2019.2943744](https://doi.org/10.1109/TED.2019.2943744)
7. D. Kumar and M. Kumar, "Comparative analysis of adiabatic logic challenges for low power CMOS circuit designs," *Microprocess. Microsyst.* **60**, 107–121 (2018) DOI: [10.1016/j.micpro.2018.04.008](https://doi.org/10.1016/j.micpro.2018.04.008)
8. Z. Wang, A. Abbasi, U. Dave, A. De Groot, S. Kumari, B. Kunert, C. Merckling, M. Pantouvaki, Y. Shi, B. Tian, K. Van Gasse, J. Verbist, R. Wang, W. Xie, J. Zhang, Y. Zhu, J. Bauwelinck, X. Yin, Z. Hens, J. Van Campenhout, B. Kuyken, R. Baets, G. Morthier, D. Van Thourhout, and G. Roelkens, "Novel light source integration approaches for silicon photonics," *Laser Photon. Rev.* **11**, 1700063 (2017) DOI: [10.1002/lpor.201700063](https://doi.org/10.1002/lpor.201700063)
9. J. Wang and Y. Long, "On-chip silicon photonic signaling and processing: a review," *Sci Bull. Fac. Agric. Kyushu Univ.* **63**, 1267–1310 (2018) DOI: [10.1016/j.scib.2018.05.038](https://doi.org/10.1016/j.scib.2018.05.038)
10. Y. Shirasaki, G. J. Supran, M. G. Bawendi, and V. Bulović, "Emergence of colloidal quantum-dot light-emitting technologies," *Nat. Photonics* **7**, 13–23 (2013) DOI: [10.1038/nphoton.2012.328](https://doi.org/10.1038/nphoton.2012.328)
11. S. Hepp, M. Jetter, S. L. Portalupi, and P. Michler, "Semiconductor quantum dots for integrated quantum photonics," *Adv. Quantum Technol.* **2**, 1900020 (2019) DOI: [10.1002/quote.201900020](https://doi.org/10.1002/quote.201900020)
12. Q. Feng, W. Wei, B. Zhang, H. Wang, J. Wang, H. Cong, T. Wang, and J. Zhang, "O-Band and C/L-Band III-V Quantum Dot Lasers Monolithically Grown on Ge and Si Substrate," *NATO Adv. Sci. Inst. Ser. E Appl. Sci.* **9**, 385 (2019) DOI: [10.3390/app9030385](https://doi.org/10.3390/app9030385)
13. H. Qian, S.-W. Hsu, K. Gurunatha, C. T. Riley, J. Zhao, D. Lu, A. R. Tao, and Z. Liu, "Efficient light generation from enhanced inelastic electron tunnelling," *Nat. Photonics* **12**, 485–488 (2018) DOI: [10.1038/s41566-018-0216-2](https://doi.org/10.1038/s41566-018-0216-2)
14. M. Parzefall, P. Bharadwaj, A. Jain, T. Taniguchi, K. Watanabe, and L. Novotny, "Antenna-coupled photon emission from hexagonal boron nitride tunnel junctions," *Nat. Nanotechnol.* **10**, 1058–1063 (2015) DOI: [10.1038/nnano.2015.203](https://doi.org/10.1038/nnano.2015.203)
15. J. Kern, R. Kullock, J. Prangasma, M. Emmerling, M. Kamp, and B. Hecht, "Electrically driven optical antennas," *Nat. Photonics* **9**, 582–586 (2015) DOI: [10.1038/nphoton.2015.141](https://doi.org/10.1038/nphoton.2015.141)
16. B. Cheng, T. Zellweger, K. Malchow, X. Zhang, M. Lewerenz, E. Passerini, J. Aeschlimann, U. Koch, M. Luisier, A. Emboras, A. Bouhelier, and J. Leuthold, "Atomic scale memristive photon source," *Light Sci Appl* **11**, 78 (2022) DOI: [10.1038/s41377-022-00766-z](https://doi.org/10.1038/s41377-022-00766-z)
17. D. Ielmini and R. Waser, *Resistive Switching: From Fundamentals of Nanionic Redox Processes to Memristive Device Applications* (Wiley-VCH Verlag GmbH, 2016).
18. I. Valov, R. Waser, J. R. Jameson, and M. N. Kozicki, "Electrochemical metallization memories--fundamentals, applications, prospects," *Nanotechnology* **22**, 254003 (2011) DOI: [10.1088/0957-4484/22/25/254003](https://doi.org/10.1088/0957-4484/22/25/254003)
19. D. Ielmini and S. Ambrogio, "16 - Neuromorphic computing with resistive switching memory devices," in *Advances in Non-Volatile Memory and Storage Technology (Second Edition)*, B. Magyari-Köpe and Y. Nishi, eds. (Woodhead Publishing, 2019), pp. 603–631 DOI: [10.1016/B978-0-08-102584-0.00017-6](https://doi.org/10.1016/B978-0-08-102584-0.00017-6)
20. G. Molas, M. Harrand, C. Nail, and P. Blaise, "9 - Advances in oxide-based conductive bridge memory (CBRAM) technology for computing systems," in *Advances in Non-Volatile Memory and Storage Technology (Second Edition)*, B. Magyari-Köpe and Y. Nishi, eds. (Woodhead Publishing, 2019), pp. 321–364 DOI: [10.1016/B978-0-08-102584-0.00010-3](https://doi.org/10.1016/B978-0-08-102584-0.00010-3)
21. Z. Ni, S. Zhou, S. Zhao, W. Peng, D. Yang, and X. Pi, "Silicon nanocrystals: unfading silicon materials for optoelectronics," *Mater. Sci. Eng. R Rep.* **138**, 85–117 (2019) DOI: [10.1016/j.mser.2019.06.001](https://doi.org/10.1016/j.mser.2019.06.001)
22. J. A. Rodríguez, M. A. Vázquez-Agustín, A. Morales-Sánchez, and M. Aceves-Mijares, "Emission Mechanisms of Si Nanocrystals and Defects in SiO₂ Materials," *J. Nanomater.* **2014**, (2014) DOI: [10.1155/2014/409482](https://doi.org/10.1155/2014/409482)
23. S. Menzel and R. Waser, "4 - Mechanism of memristive switching in OxRAM," in *Advances in Non-Volatile Memory and Storage Technology (Second Edition)*, B. Magyari-Köpe and Y. Nishi, eds. (Woodhead Publishing, 2019), pp. 137–170 DOI: [10.1016/B978-0-08-102584-0.00005-X](https://doi.org/10.1016/B978-0-08-102584-0.00005-X)
24. A. E. Owen, P. G. Le Comber, G. Sarraयरouse, and W. E. Spear, "New amorphous-silicon electrically programmable nonvolatile switching device," *IEEE Proceedings I (Solid-State and Electron Devices)* **129**, 51–54 (1982) DOI: [10.1049/ip-i-1.1982.0009](https://doi.org/10.1049/ip-i-1.1982.0009)
25. A. E. Owen, P. G. L. E. Comber, J. Hajto, M. J. Rose, and A. J. Snell, "Switching in amorphous devices," *Int. J. Electron.* **73**, 897–906 (1992) DOI: [10.1080/00207219208925733](https://doi.org/10.1080/00207219208925733)
26. B. I. Craig and R. J. Watson, "Observation of a conducting filament within unhydrogenated amorphous silicon," *J. Non-Cryst. Solids* **217**, 106–110 (1997) DOI: [10.1016/S0022-3093\(97\)00112-9](https://doi.org/10.1016/S0022-3093(97)00112-9)
27. E. Kim, Y. H. Lee, C. Chen, and T. Pang, "Vacancies in amorphous silicon: A tight-binding molecular-dynamics simulation," *Phys. Rev. B Condens. Matter* **59**, 2713–2721 (1999) DOI: [10.1103/physrevb.59.2713](https://doi.org/10.1103/physrevb.59.2713)
28. M. W. Cleveland and M. J. Demkowicz, "Persistence of negative vacancy and self-interstitial formation energies in atomistic models of amorphous silicon," *Phys. Rev. Materials* **6**, 013611 (2022) DOI: [10.1103/PhysRevMaterials.6.013611](https://doi.org/10.1103/PhysRevMaterials.6.013611)
29. M. J. Powell, S. C. Deane, and R. B. Wehrspohn, "Microscopic mechanisms for creation and removal of metastable dangling bonds in hydrogenated amorphous silicon," *Phys. Rev. B Condens. Matter* **66**, 155212 (2002) DOI: [10.1103/PhysRevB.66.155212](https://doi.org/10.1103/PhysRevB.66.155212)
30. J. Yao, L. Zhong, D. Natelson, and J. M. Tour, "In situ imaging of the conducting filament in a silicon oxide resistive switch," *Sci. Rep.* **2**, 242 (2012) DOI: [10.1038/srep00242](https://doi.org/10.1038/srep00242)
31. A. Mehonic, S. Cuff, M. Wojdak, S. Hudziak, C. Labbé, R. Rizk, and A. J. Kenyon, "Electrically tailored resistance switching in silicon oxide," *Nanotechnology* **23**, 455201 (2012) DOI: [10.1088/0957-4484/23/45/455201](https://doi.org/10.1088/0957-4484/23/45/455201)
32. V. A. Gritsenko, D. V. Gritsenko, Y. N. Novikov, R. W. M. Kwok, and I. Bello, "Short-range order, large-scale potential fluctuations, and photoluminescence in amorphous Si_Nx," *J. Exp. Theor. Phys.* **98**, 760–769 (2004) DOI: [10.1134/1.1757676](https://doi.org/10.1134/1.1757676)
33. X. Jiang, Z. Ma, H. Yang, J. Yu, W. Wang, W. Zhang, W. Li, J. Xu, L. Xu, K. Chen, X. Huang, and D. Feng, "Nanocrystalline Si pathway induced unipolar resistive switching behavior from annealed Si-rich Si_Nx/Si_Ny multilayers," *J. Appl. Phys.* **116**, 123705 (2014) DOI: [10.1063/1.4896552](https://doi.org/10.1063/1.4896552)