

Demonstration of SiN-photonic Controlled-NOT Gate using Path-entangled Qubit Pairs from a Si-photonic Quantum Splitter

Jong-Moo Lee^{(1)*}, Wook-Jae Lee⁽¹⁾, Min-su Kim⁽¹⁾, Sungwan Cho⁽¹⁾, Gabriele Navickaite⁽²⁾, Juan Fernandez⁽³⁾, and Jung Jin Ju⁽¹⁾

⁽¹⁾ Electronics and Telecommunications Research Institute, Daejeon 34129, Korea

^{(1)*} jongmool@etri.re.kr

⁽²⁾ LIGENTEC SA, Lausanne, Switzerland

⁽³⁾ VLC Photonics S.L., Camino de Vera s/n - 46021 Valencia, Spain

Abstract We fabricate a silicon-nitride photonic integrated circuit (PIC) to demonstrate a controlled-NOT quantum-logic gate and a silicon PIC generating path-entangled qubit pairs by splitting degenerate photon pairs. We measure 81% fidelity of CNOT using the fully packaged PICs.

Introduction

Recent progress of quantum computing with the possibility of quantum supremacy [1] inspires the photonic-integrated-circuit (PIC) technology to expand its application to quantum information technology (QIT) [2-5]. Photons are attractive in QIT because they do not interact with the environment and can be transferred through the fiber-optic networks. There have been many efforts using photons on demonstrating quantum logic operations [2-4] and quantum repeaters [5-7] which are essential for the quantum computing and networks.

Silicon PICs are very useful in QIT with the merit of the high-density integration and the high nonlinearity to generate photon pairs by the spontaneous four-wave mixing (SWFM) [8,9]. Silicon-nitride (SiN) PICs are attractive in QIT

with the merit in low optical loss (0.1dB/cm, 1.5dB/facet), low thermo-optic coefficient ($dn/dT \sim 10^{-5}/K$), and the negligible polarization dependence [10,11]. Quantum interference through a SiN PIC has been demonstrated using single-photon qubits from a bulk laser system [10] and the possibility of the controlled-NOT (CNOT) gate using a SiN PIC has been estimated by classical measurements of transmission spectrum through the SiN PIC without using single-photon qubits in the reference [11].

Here, we demonstrate the quantum logic operation of the CNOT gate through a SiN PIC, by using the path-entangled qubit pairs from a Si PIC.

Experiments

Figure 1 shows the schematic drawing and photos of the experimental setup to generate the

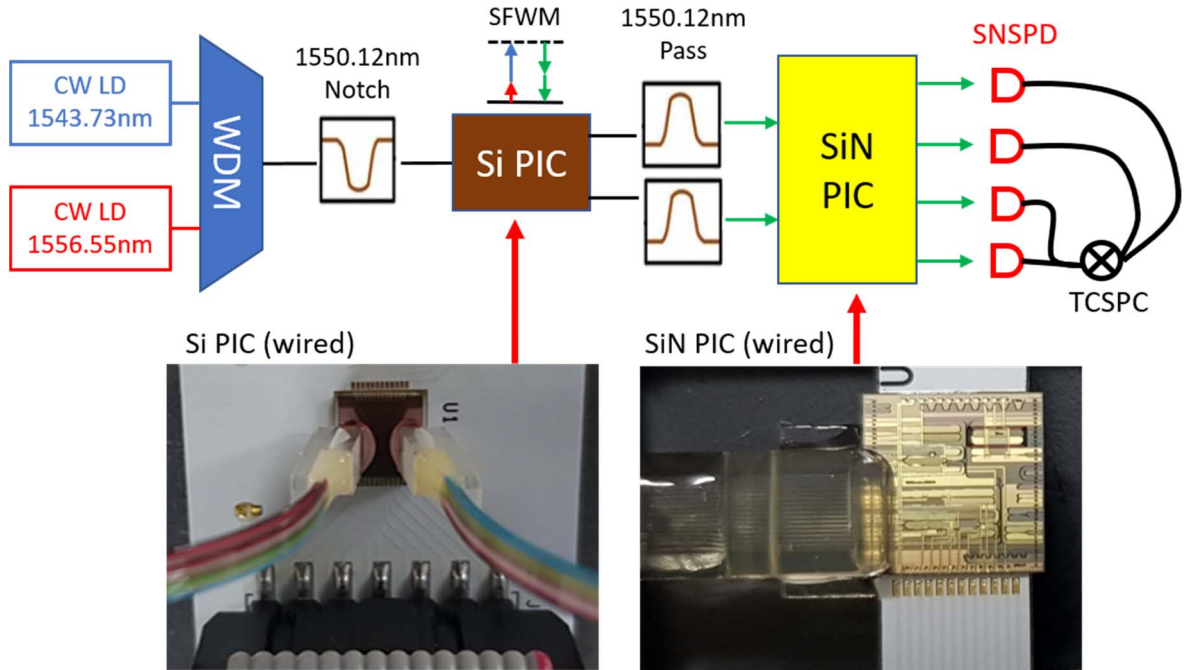


Fig. 1: Schematic drawing and photos for the experimental setup of qubit-pair generation and CNOT operation.

path-entangled qubit pairs and measure the CNOT gate operation. The Si PIC and SiN PIC are optically wired for the connection of photonic signals and electrically wired to adjust the optical phase of the PICs as in Fig. 1. Noise filtering is important in measuring quantum effect as in our previous results in reference [9] and we include a notch filter and bandpass filters as in Fig. 1. Identical photon pairs are generated through the Si waveguide by degenerate SFWM and the identical pairs are split through the quantum splitter as in Fig. 2, by the similar way to the reference [8]. The split state of qubit pairs is used as the input to the SiN CNOT gate after the noise filtering through the bandpass filters. The photons after the logic operation through the CNOT gate are detected by superconducting-nanowire single-photon detectors (SNSPD). The detected single-photon signals are processed to the quantum correlation results using the time-correlated single-photon counting (TCSPC) technique.

technique.

Figure 3 shows the schematic drawing and photos of SiN CNOT gate fabricated in this experiment. The CNOT gate is operated by linear-optic quantum interference based on Hong-Ou-Mandel (HOM) effect [11]. We use the path-entangled photon qubits from the Si PIC as the input to the CNOT gate and measure the out photons from the output ports depending on the input states.

Measured Results

Figure 4 shows the measured correlation counts of the path-entangled photon pairs from the Si PIC. The results in Fig. 4 clearly show the degenerate photon pairs split into path-entangle qubit pairs.

Figure 5 shows the measured second-order correlation $g^{(2)}(0)$ of the heralded single-photon qubit from the Si PIC. The path-entangle photon shows the single-photon property with $g^{(2)}(0)$ less

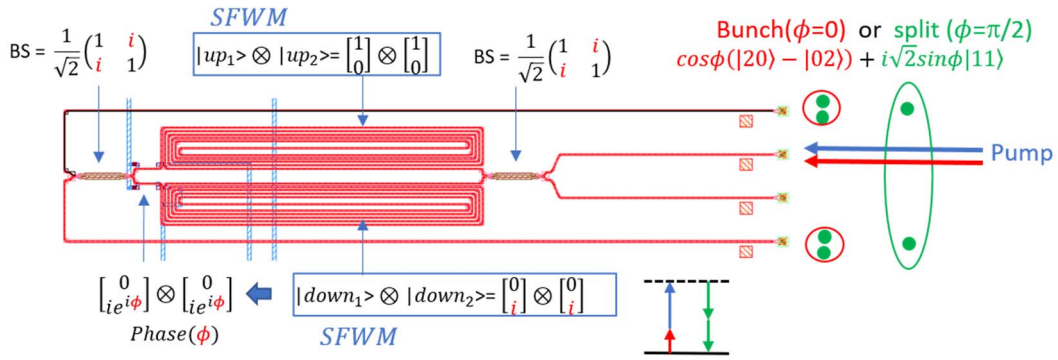


Fig. 2: Schematic drawing of the quantum splitter to generate and split photon pairs into the path-entangled qubits

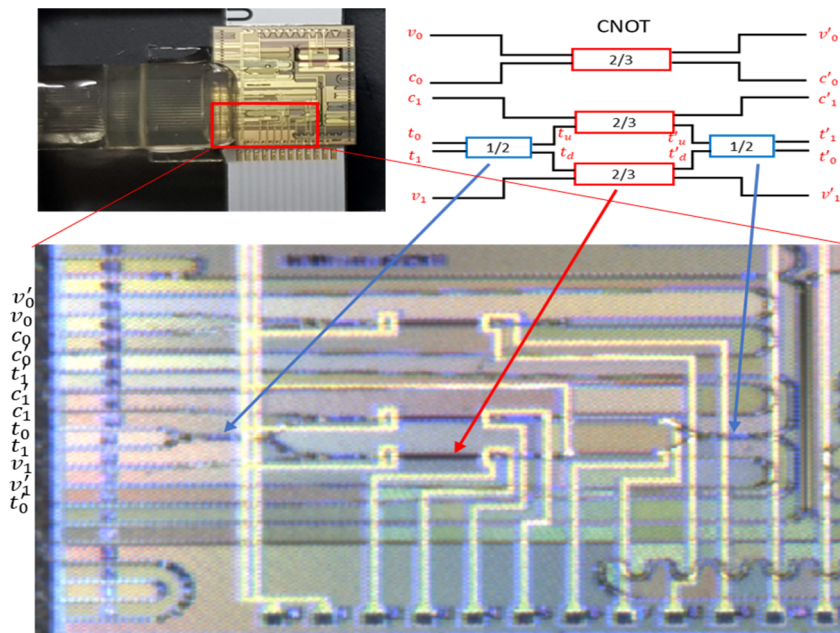


Fig. 3: Schematic drawing and photos of SiN CNOT gate.

than 0.05 in Fig. 5.

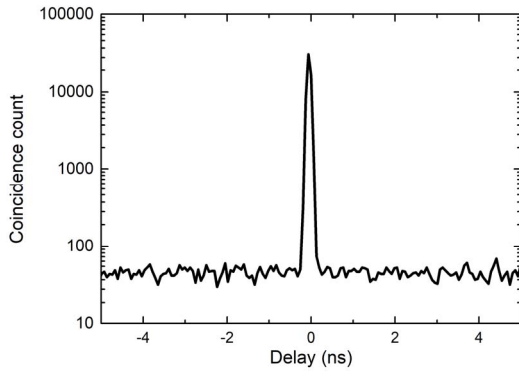


Fig. 4: Measured correlation counts of the path-entangled photon pairs from the Si PIC.

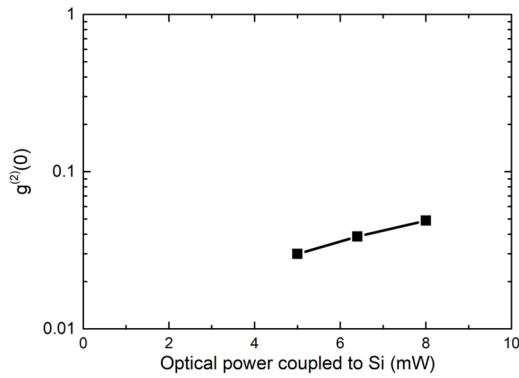


Fig. 5: Measured second-order correlation $g^{(2)}(0)$ of the heralded single-photon qubit from the Si PIC.

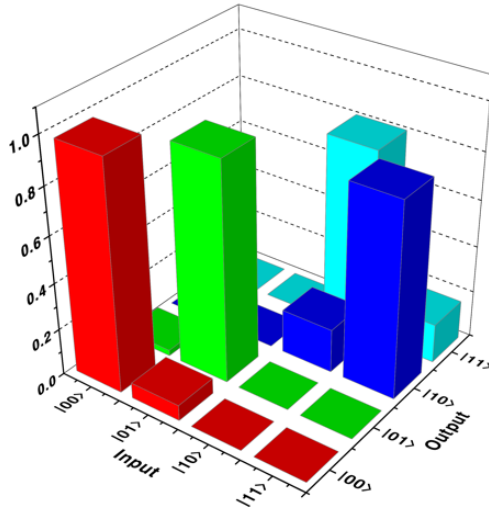


Fig. 6: Measured output qubit states through the SiN CNOT gate depending on the input path-qubit states.

Figure 6 shows the measured output qubit states through the SiN CNOT gate depending on the input entangled-path qubit states. The results in Fig. 5 clearly show the 2-qubit state changes to $|11\rangle$ from $|10\rangle$ input and to $|10\rangle$ from $|11\rangle$ input. The fidelity of the signal is about 81% in Fig. 5,

and we expect it can be improved by reducing multi-photon noises from the input photon qubits. These changes of the quantum states are due to the quantum interference between the single-photon qubits and the CNOT operation can be useful in the future application of photons to QIT such as quantum computing and quantum repeaters.

Conclusions

We demonstrated a SiN PIC operating as the quantum-logic CNOT gate with the fidelity of 81%, by using the quantum-entangled qubit pairs from a Si PIC as the input qubits to the SiN PIC. This result shows the bright possibility of the combination of Si and SiN PICs for the quantum information process.

Acknowledgements

We fabricated the Si PIC including the photon-pair source at IMEC through Europractice, and the SiN PIC including the CNOT gate at Ligentec through VLC Photonics. The authors acknowledge the financial support of this research by IITP (20170000740011001) and NRF (2020M3E4A1078453) funded by the Korean government (MSIT).

References

- [1] F. Arute, K. Arya, R. Babbush et al. "Quantum supremacy using a programmable superconducting processor," *Nature*, vol. 574, pp. 505–510 (2019).
- [2] J. L. O'Brien, "Optical Quantum Computing", *Science*, vol. 318, 5856, pp. 1567–1570 (2007).
- [3] F. Flamini, N. Spagnolo and F. Schiarrino, "Photonic quantum information processing: a review", *Rep. Prog. Phys.*, vol. 82, no. 1, 016001 (2019).
- [4] J. Wang, F. Sciarrino, A. Laing, M. G. Thompson, "Integrated photonic quantum technologies," *Nat. Photonics*, vol. 14, pp. 273–284 (2020).
- [5] D. Llewellyn et al., "Chip-to-chip quantum teleportation and multi-photon entanglement in silicon", *Nat. Physics*, vol. 16, pp. 148–153 (2020).
- [6] K. Azuma, K. Tamaki, and H. Lo, "All-photonic quantum repeaters," *Nat Commun*, vol. 6, 6787 (2015).
- [7] Y. Hasegawa et al., "Experimental time-reversed adaptive Bell measurement towards all-photonic quantum repeaters," *Nat Commun*, vol. 10, 378 (2019).
- [8] J. W. Silverstone et al., "On-chip quantum interference between silicon photon-pair sources", *Nat. Photonics*, vol. 8, pp. 104–108 (2014).
- [9] J.-M. Lee et al., "Noise Filtering for Highly Correlated Photon Pairs From Silicon Waveguides", *J. Lightwave Technol.*, vol. 37, pp. 5428–5434 (2019).
- [10] C. Taballione et al., "8×8 reconfigurable quantum photonic processor based on silicon nitride waveguides," *Opt. Express*, vol. 27, pp. 26842–26857 (2019).
- [11] M. Poot et al., "Design and characterization of integrated components for SiN photonic quantum circuits", *Opt. Express*, vol. 24, no. 7, pp. 6843–6860 (2016).